

Revision History

Revision	Date	Comment
V1.0	01/05/07	1.Initial Release.
V1.1	07/10/07	1.Add a C4 1pF cap between DP and DM
V1.2	08/30/07	1.Remove R9, R10 resistors from pin #35 and pin #36
		2.Add U5, R11, R13, C40, C41, C42, C43 and L3 in the switching regulator reference circuit block of external device for AX88172A MAC-to-MAC Bus-power mode applications
		3.Connect the GPIO_0 (Power Management Pin Control)signal to pin #35 and connect theGPIO_1 (External Device_Reset Pin Control) signal to pin #36.
		4.Change the pins description of J1 header from the pin definition of MAC-MII mode to the pin definition of PHY-Rev-MII (Dual PHY). PIN #6~9:Changed from RXD3~0 to TXD3~0 PIN #10:Changed from RXDV to TXEN PIN #11:Changed from RXCLK to TXCLK PIN #14:Changed from TXCLK to RXCLK PIN #15:Changed from TXEN to RXDV PIN #16~19:Changed from TXD3~0 to RXD3~0
V1.30	05/18/2009	1.Add some important note messages in Page 1. 2.Change the revision format to x.xx.

ASIX ELECTRONICS CORPORATION		
Title Revision History		
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